

Workshop for development and applications of fast-timing semiconductor devices

8th Dec 2018

@ University of Tsukuba,
Tokyo Campus (Otsuka) Room 117/119

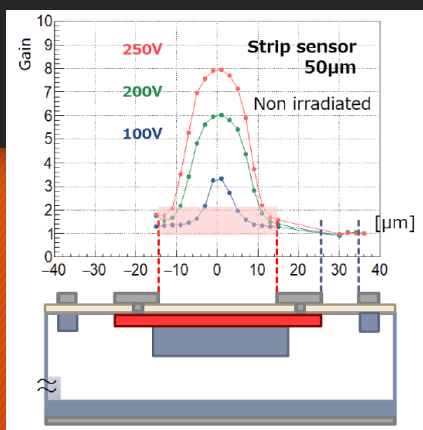
Registration: <https://indico.cern.ch/e/LGADatUTsukuba>
Send e-mail to hara@hep.px.tsukuba.ac.jp for presentation

Since the several successful operation demonstrations of low-gain avalanche detectors (LGAD), we are in a stage of further investigation on development and application possibilities using such fast-timing, ca. 30 ps, semiconductor devices which should realise a superior position resolution of O(10microns) at the same time. The workshop is organised to summarise the LGAD development status and to discuss further R&D activities

Organizing members

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5min walk Myogadani
(Tokyo Metro
Marunouchi-line)

